

IN THE ABSTRACT:

Please delete the Abstract at page 11 in its entirety and substitute the following new Abstract:

[illegible]

--ABSTRACT

1
2 The present invention relates to a modular interconnection architecture for an expandable
3 multiprocessor machine. It comprises a first interconnection level (MI) comprising connection
4 agents (NCS_i) that connect the multiprocessor modules and handle the transactions between the
5 multiprocessor modules, and a second interconnection level (SI) comprising external connection
6 nodes (NCE_j) that connect the nodes (N_j) to one another and handle the transactions between the
7 nodes (N_j). Each external connection node (NCE_j) comprises two connection agents identical to
8 the connection agent (NCS_i), connected head-to-tail, one of the two agents (NCS'_j) receives and
9 filters the transactions sent by the node (N_j) to which it is connected. The other agent (NCS''_j)
10 receives and filters the transactions sent by the other nodes (N_j) to which it is connected. Its
11 applications specifically include the construction of an entire range of machines: UMA, QUASI-
12 UMA, NUMA, cluster, etc.--
